Jordan Ansari Work Log

Milestone 1:

Sat, April 10, 2021: Met with team [30 min]

We agreed upon an accumulator processor, as it would be unique from MIPS but still usable and interesting to create. We also started working on the document for milestone 1. Most of the time was spent on the things which did not require much deliberation (such as necessary instructions and registers).

Sun, April 11, 2021: Met with team [45 min]

We continued to work on our design document, coming to a few important decisions- first, we realized we would need more registers than a single accumulator for our design to work. We agreed to add more accumulator registers, as well as temporary, stored, and argument registers. The rest of the night was spent adding to our design document- tomorrow we intend to work on the actual example code and its machine translation.

Mon, April 12, 2021: Met with team [45 min]

I worked on writing the machine code for Euclid’s Algorithm. While writing it, I realized we’d need a few more procedures (set, setc), as well as more registers ($0, $v1-v2). We added these to accommodate these realizations, and the rest of the team worked on fine-tuning and coming up with opcodes to transform the assembly code into the machine language translation.